



Notice for Ph.D Admission Test (Pre-Qualifying Exam)

As per the notice no. Advt./Acad/2025/03 dated 29/07/2025 for Ph.D. admission under **Visvesvaraya Scheme, for Electronics and IT, Phase - II** (Academic Year: 2025-26) the Pre-Qualifying Exam (Written Test) is scheduled as follows:

Date of Exam (Written -Cum-Interview): 02/09/2025

Reporting Time: 10:30 AM

Written Test Time: 11:00 AM

Interview Reporting Time: 01:00 PM

Venue: Shrimant Madhav Rao Scindia Knowledge Centre, MITS-DU, Gwalior.

Following applicant for **Ph.D. admission under Visvesvaraya Scheme** are required to appear for Pre-Qualifying Exam (Written Test). All must ensure to appear for the exam with original identity proof.

No TA/DA shall be payable to any applicant.

S. No.	Name of Applicant	Applied for Ph.D. Discipline	Discipline for Pre-Qualifying Exam (Entrance Test)
1.	Jaydeep Singh Parmar	Electronics	Electronics Engineering
2.	Abhishek Kulshrestha	Electronics	Electronics Engineering
3.	Meenu Shukla	Information Technology	Information Technology
4.	Yamini Richhariya	Information Technology	Information Technology

Following applicants are eligible for the **direct interview for Ph.D admission under Visvesvaraya Scheme** based on the valid GATE/NET Card.

S. No.	Name of Applicant	Applied for Ph.D. Discipline
1.	Bobby Raje	Electronics
2.	Sahiv Singh Kanojiya	Information Technology