Notice for Ph.D Admission Test (Pre-Qualifying Exam)

As per the **notice no. Advt./Acad/2025/03 dated 14/10/2025** for **Ph.D. admission under Visvesvaraya Scheme, for Electronics and IT, Phase - II (Academic Year: 2025-26)** the Pre-Qualifying Exam (Written Test) is scheduled as follows:

Date of Exam (Written -Cum-Interview): 16/12/2025
Reporting Time: 09:30 AM
Written Test Time: 10:30 AM
Interview Reporting Time (For those qualifying the written test): 02:00 PM
Venue: Shrimant Madhav Rao Scindia Knowledge Centre, MITS-DU, Gwalior.

Following applicant for **Ph.D. admission under Visvesvaraya Scheme** are required to appear for Pre-Qualifying Exam (Written Test). The duration of exam will be 02 hours. 50% of the questions will be related to research methodology and quantitative aptitude, and the remaining 50% will be domain-specific. All must ensure to appear for the exam with original identity proof.

No TA/DA shall be payable to any applicant.

S. No.	Name of Applicant	Applied for Ph.D. Discipline	Discipline for Pre-Qualifying Exam (Entrance Test)
1.	Rishi Kant Sharma	Electronics	Electronics Engineering
2.	Sachindra Kumar Verma	Electronics	Electronics Engineering
3.	Rinki Pakshwar	Information Technology	Information Technology
4.	Poonam Ramashankar Pathak	Information Technology	Information Technology

Following applicant is eligible for the **direct interview for Ph.D. admission under Visvesvaraya Scheme** based on the valid GATE/NET Card (subject to verification).

S. No.	Name of Applicant	Applied for Ph.D. Discipline
1.	Mahendra Gupta	Electronics

Note:
- Applicant must bring original documents of their academic credentials, applicable scorecards, category certificate, experience certificate, identity proof and no objection certificate from employer (if applicable) at the time of reporting for document verification.
- The admission of the candidate is subject to verification of original documents.
- No TA/DA shall be payable to any applicant.

Deputy Registrar (Academics)
04/12/2025