



Notice for Ph.D Admission Test (Pre-Qualifying Exam)

As per the notice no. Advt./Acad/2025/03 dated 29/07/2025 for Ph.D. admission under Visvesvaraya Scheme, for Electronics and IT, Phase - II (Academic Year: 2025-26) the Pre-Qualifying Exam (Written Test) (Third Cycle), is scheduled as follows:

Date of Exam (Written -Cum-Interview): 15/09/2025

Reporting Time: 11:00 AM

Written Test Time: 11:30 AM

Interview Reporting Time: 01:30 PM

Venue: Shrimant Madhav Rao Scindia Knowledge Centre, MITS-DU, Gwalior.

Following applicant for **Ph.D. admission under Visvesvaraya Scheme** are required to appear for Pre-Qualifying Exam (Written Test). Applicant must ensure to appear for the exam with original identity proof.

No TA/DA shall be payable to any applicant.

S. No.	Name of Applicant	Applied for Ph.D. Discipline	Discipline for Pre-Qualifying Exam (Entrance Test)
1.	Meenu Shukla	Information Technology	Information Technology

Following applicant are eligible for the **direct interview for Ph.D admission under Visvesvaraya Scheme** based on the valid GATE/NET Score.

S. No.	Name of Applicant	Applied for Ph.D. Discipline
1.	Vinay Gupta	Electronics

Dean (Academics)
Madhav Institute of Technology & Science
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