

**Madhav Institute of Technology & Science Gwalior (M.P.)**  
(A Govt. Aided UGC Autonomous Institute Affiliated to RGPV, Bhopal)

**NAAC Accredited with A++ Grade**

**Centre for Artificial Intelligence**

**Mid Semester-I Exam Date Sheet of II and III Year (Session: Jan.-June 2024)**

<b>Course Code</b>	<b>Course Name</b>	<b>Branch</b>	<b>Semester</b>	<b>Batch</b>	<b>Date</b>	<b>Time</b>	<b>Room No.</b>
240605	AI for Robotics	AIR	VI	A	26.02.2024	12:00 to 01:00 PM	M103
240605	AI for Robotics	AIR	VI	B	26.02.2024	12:00 to 01:00 PM	M201
240606	Image Processing	AIR	VI	A	27.02.2024	10:30 to 11:30 AM	SH2
240606	Image Processing	AIR	VI	B	27.02.2024	10:30 to 11:30 AM	SH2
240607	Artificial Intelligence & Machine Learning	AIR	VI	A	28.02.2024	12:00 to 01:00 PM	M103
240607	Artificial Intelligence & Machine Learning	AIR	VI	B	28.02.2024	12:00 to 01:00 PM	M201
1000007	Intellectual Property Rights (IPR)	AIR	VI	A	29.02.2024	10:30 to 11:30 AM	SH2
1000007	Intellectual Property Rights (IPR)	AIR	VI	B	29.02.2024	10:30 to 11:30 AM	SH2
270601	Data Mining & Warehousing	AIDS	VI	A	26.02.2024	02:00 to 03:00 PM	SH2
270601	Data Mining & Warehousing	AIDS	VI	B	26.02.2024	02:00 to 03:00 PM	SH2
270602	Image Processing	AIDS	VI	A	27.02.2024	12:00 to 01:00 PM	SH2
270602	Image Processing	AIDS	VI	B	27.02.2024	12:00 to 01:00 PM	SH2
270603	Deep Learning	AIDS	VI	A	28.02.2024	12:00 to 01:00 PM	M202
270603	Deep Learning	AIDS	VI	B	28.02.2024	12:00 to 01:00 PM	M203
1000007	Intellectual Property Rights (IPR)	AIDS	VI	A	29.02.2024	10:30 to 11:30 AM	M103
1000007	Intellectual Property Rights (IPR)	AIDS	VI	B	29.02.2024	10:30 to 11:30 AM	M201
280601	Data Mining & Warehousing	AIML	VI	A	26.02.2024	02:00 to 03:00 PM	M103
280601	Data Mining & Warehousing	AIML	VI	B	26.02.2024	02:00 to 03:00 PM	M201
280602	Image Processing	AIML	VI	A	27.02.2024	02:00 to 03:00 PM	SH2
280602	Image Processing	AIML	VI	B	27.02.2024	02:00 to 03:00 PM	SH2
280603	Deep Learning	AIML	VI	A	28.02.2024	12:00 to 01:00 PM	SH2
280603	Deep Learning	AIML	VI	B	28.02.2024	12:00 to 01:00 PM	SH2
1000007	Intellectual Property Rights (IPR)	AIML	VI	A	29.02.2024	10:30 to 11:30 AM	M202
1000007	Intellectual Property Rights (IPR)	AIML	VI	B	29.02.2024	10:30 to 11:30 AM	M203
910126	Information Security (OC-1)	-	VI	A	01.03.2024	04:00 to 05:00 PM	M103
910126	Information Security (OC-1)	-	VI	B	01.03.2024	04:00 to 05:00 PM	M201

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**Mid Semester-I Exam Date Sheet of II and III Year (Session: Jan.-June 2024)**

Course Code	Course Name	Branch	Semester	Batch	Date	Time	Room No.
2240421	Calculus and Optimization Techniques	AIR	IV	A	26.02.2024	10:30 to 11:30 AM	M103
2240421	Calculus and Optimization Techniques	AIR	IV	B	26.02.2024	10:30 to 11:30 AM	M201
3000004	Language	AIR	IV	A	26.02.2024	04:00 to 05:00 PM	M101
3000004	Language	AIR	IV	B	26.02.2024	04:00 to 05:00 PM	M102
2240422	Modern Computer Architecture	AIR	IV	A	27.02.2024	12:00 to 01:00 PM	M103
2240422	Modern Computer Architecture	AIR	IV	B	27.02.2024	12:00 to 01:00 PM	M201
1000001	Indian Constitution and Traditional Knowledge	AIR	IV	A	27.02.2024	04:00 to 05:00 PM	M203
1000001	Indian Constitution and Traditional Knowledge	AIR	IV	B	27.02.2024	04:00 to 05:00 PM	SH2
2240423	Control Systems	AIR	IV	A	28.02.2024	04:00 to 05:00 PM	M203
2240423	Control Systems	AIR	IV	B	28.02.2024	04:00 to 05:00 PM	M202
2240424	Robot Kinematics	AIR	IV	A	29.02.2024	04:00 to 05:00 PM	M101
2240424	Robot Kinematics	AIR	IV	B	29.02.2024	04:00 to 05:00 PM	M102
2240425	Information Security	AIR	IV	A	01.03.2024	10:30 to 11:30 AM	M102
2240425	Information Security	AIR	IV	B	01.03.2024	10:30 to 11:30 AM	M201
2270421	Calculus and Optimization Techniques	AIDS	IV	A	26.02.2024	10:30 to 11:30 AM	M202
2270421	Calculus and Optimization Techniques	AIDS	IV	B	26.02.2024	10:30 to 11:30 AM	M203
3000004	Language	AIDS	IV	A	26.02.2024	04:00 to 05:00 PM	M201
3000004	Language	AIDS	IV	B	26.02.2024	04:00 to 05:00 PM	M202
2270422	Modern Computer Architecture	AIDS	IV	A	27.02.2024	12:00 to 01:00 PM	M202
2270422	Modern Computer Architecture	AIDS	IV	B	27.02.2024	12:00 to 01:00 PM	M203
1000001	Indian Constitution and Traditional Knowledge	AIDS	IV	A	27.02.2024	04:00 to 05:00 PM	M201
1000001	Indian Constitution and Traditional Knowledge	AIDS	IV	B	27.02.2024	04:00 to 05:00 PM	M202
2270423	Software Engineering	AIDS	IV	A	28.02.2024	04:00 to 05:00 PM	M201
2270423	Software Engineering	AIDS	IV	B	28.02.2024	04:00 to 05:00 PM	M103
2270424	Cloud Computing and Virtualization	AIDS	IV	A	29.02.2024	04:00 to 05:00 PM	M103
2270424	Cloud Computing and Virtualization	AIDS	IV	B	29.02.2024	04:00 to 05:00 PM	M201

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Course Code	Course Name	Branch	Semester	Batch	Date	Time	Room No.
2270425	Information Security	AIDS	IV	A	01.03.2024	04:00 to 05:00 PM	M202
2270425	Information Security	AIDS	IV	B	01.03.2024	04:00 to 05:00 PM	M203
2280421	Calculus and Optimization Techniques	AIML	IV	A	26.02.2024	12:00 to 01:00 PM	M202
2280421	Calculus and Optimization Techniques	AIML	IV	B	26.02.2024	12:00 to 01:00 PM	M203
3000004	Language	AIML	IV	A	26.02.2024	04:00 to 05:00 PM	M203
3000004	Language	AIML	IV	B	26.02.2024	04:00 to 05:00 PM	SH2
2280422	Modern Computer Architecture	AIML	IV	A	27.02.2024	10:30 to 11:30 AM	M103
2280422	Modern Computer Architecture	AIML	IV	B	27.02.2024	10:30 to 11:30 AM	M201
1000001	Indian Constitution and Traditional Knowledge	AIML	IV	A	27.02.2024	04:00 to 05:00 PM	M102
1000001	Indian Constitution and Traditional Knowledge	AIML	IV	B	27.02.2024	04:00 to 05:00 PM	M103
2280423	Software Engineering	AIML	IV	A	28.02.2024	04:00 to 05:00 PM	M102
2280423	Software Engineering	AIML	IV	B	28.02.2024	04:00 to 05:00 PM	M101
2280424	Cloud Computing and Virtualization	AIML	IV	A	29.02.2024	04:00 to 05:00 PM	M202
2280424	Cloud Computing and Virtualization	AIML	IV	B	29.02.2024	04:00 to 05:00 PM	M203
2280425	Information Security	AIML	IV	A	01.03.2024	10:30 to 11:30 AM	M202
2280425	Information Security	AIML	IV	B	01.03.2024	10:30 to 11:30 AM	M203

*Note: Batch-A will be from Enrolment 1 to 40; and Batch-B will be from Enrolment 41 onwards including Lateral Entry and Branch change students*



**Dr. Mir Shahnawaz Ahmad**  
Time-Table Coordinator



**Ms. Shubha Mishra**  
Time-Table Coordinator



**Dr. Rajni Ranjan Singh**  
Coordinator, Centre for Artificial Intelligence