MADHAV INSTITUTE OF TECHNOLOGY & SCIENCE, GWALIOR (M.P.) (A Govt. Aided UGC Autonomous & NAAC Accredited Institute Affiliated to RGPV, Bhopal) Department of Computer Science and Engineering

Workshop Report

Type of activity:	Workshop (Institute level)			
Title of the activity:	Six-day workshop on "GATE Module Training & Placement Preparation "			
Number of participants:	92			
Organized by:	Department of CSE and IEEE student chapter, MITS Gwalior			
Convener:	Dr. Manish Dixit, Professor & Head			
Coordinators:	Dr. Ranjeet Kumar Singh, Assistant Professor. Dr. Kalka Dubey Assistant Professor			
Brief description of the activity:	 This Workshop is pinioned towards generating and enhancing awareness about how can solve the technical problem of placement activity. This event is geared to provide and enhanced the understanding analysing power of our students. 			
Objectives of the activity:	 To provide understanding fundamentals concepts of Computer science and Engineering Subject. To solve and analyse the various previous year Placement paper and GATE paper. To enhanced the technical skill and developed problem- solving approach in our students. 			
Outcomes of the activity:	 The participants will be able to: 1. To able to solve technical problem of Industrial Platform 2. They are able to analysed and solve the conceptual and technical problem of computer science and engineering. 3. They know about how can solve different type of problem in placement activity. 			
Mode of delivery:	Offline			
Platform for conducting the activity:	Whiteboard			
Name of the Experts and Description of delivered work:	Dr. Manish Dixit, Professor & Head Dr. Ranjeet Kumar Singh Dr. Kalka Dubey			

Date: Participants:	ipants: B.Tech(CSE/IT/AIR/IT IOT/EE IOT/AIDS/AIML/MCA/EC/ET	
Duration:	Three day (First Phase of Workshop)	
	Day Three started with the Compiler and Automata theory subjects and the important topics of these are Language, Grammar, First and Follow, Parser are covered. After covering these topic Dr Manish Dixit distributed the GATE study material including the subjects: Algorithm, Data Structure, Operating System, Theory of Computation, Compiler, Computer Network, and DBMS. These notes are very useful for the preparation of the GATE examination as well as students gain the knowledge of the core subjects which is further improve the preparation of campus placement.	
	Now, second day, Dr. Ranjeet kumar singh and Dr. Kalka Dubey started with Database Management System (DBMS) and covered the important topics. DBMS is also an very important subject for the point of GATE examination and campus placement. Primary Key, Candidate Key, Normalization, Decomposition, Transaction, topics are covered from this workshop. Also students solve so many numerical problems related to these topics.	
	Dr. Ranjit Kumar Singh and Dr. Kalka Dubey started the C programming and Data Structure subject. The most important topic of both the subjects including the Pointer, Pointer in Array, Pointer in String, Link List, Time Complexity Analysis, etc. are covered. Furthermore, for the practicing the students so many example are discussed in the class for more clarification. Students are also showed enthuses while solving numeric problem related to these topics which are very useful in the GATE examination as well as good for the campus placement.	
	First day, Dr Manish Dixit, represented the department of computer science and engineering & IEEE students chapter Gwalior, He explained the benefits of GATE examinations. The GATE exam's purpose is to test students' knowledge and understanding of their graduate-level subjects in engineering and science. It is one of the most prestigious exams for engineers aiming to secure a job in PSU and Govt. sectors or pursue higher education in the premier institutes such as IIT's/NIT's/IISc etc. He also explained how to start the preparation for the GATE examination. Furthermore, he gave a short overview how to prepared for campus placement.	





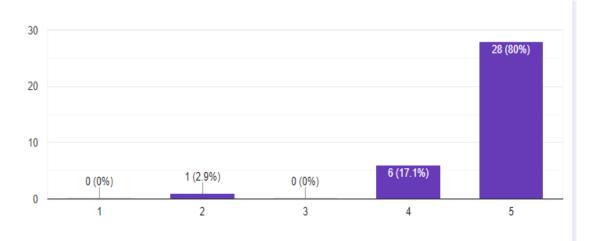




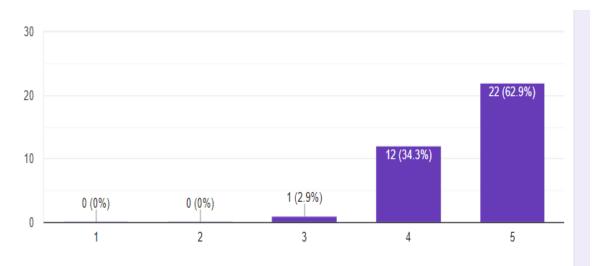


Workshop Feedback Analysis

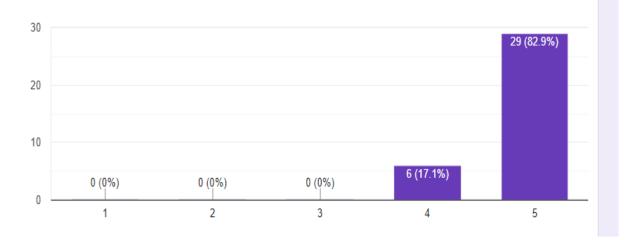
The workshop contained useful information and start according to scheduled



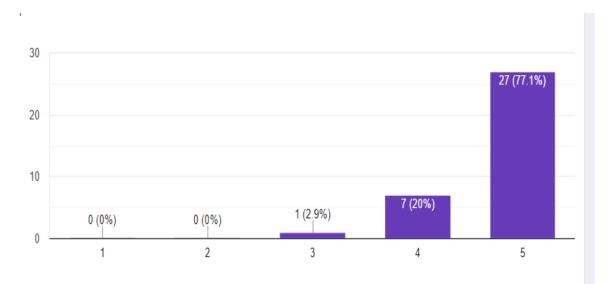
The quality of contain of the first phase of the workshop meet my exceptions:



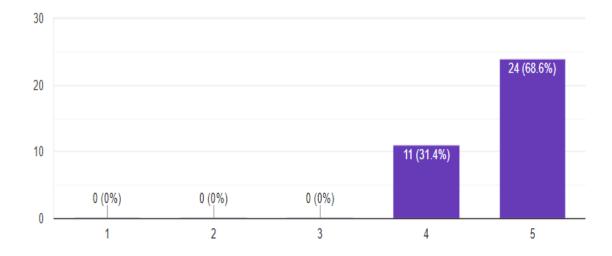
The Instructors were knowledgeable on the topics of workshop



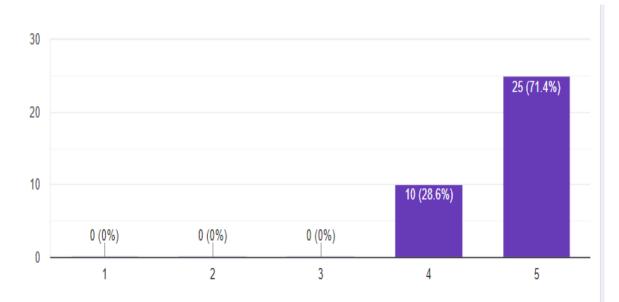
The information and knowledge gained during the workshop is helpful for my career:



After attending this workshop, I feel it clear mine doubts which were answered very well:



The overall rating of this phase of workshop:



Workshop Attendance

Department of Computer Science and Engineering

Department of Computer Science and Engineering & IEEE Student Chapter is organize a workshop on "GATE Module Training&Placement Preparation activity", was heldon 08.04.2022 to 10.04.2022 The following students were present:

S.No.	Name	Department	Signature
1.	Sochin Yodar	MCA	Sochh
2.	chierag Soni	MCA	chieres-
3.	Rohit Monkele	MCA	(8th)
4.	Varisha Sharma	mcA Jstyn	shapme -
5.	Shivani Rubey	MCA 1stys.	Dubert
6.	Sonali Maguya	MCA 1 Styles	
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8.	Roj Poratap Rojawat	MCA Ptymo	(Py
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10.	Sneha Nigam.	B. Tech 32d	Snell
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	Aushal Queesli ET	B-tech - 87	rd VPCW Alsh
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27.	Anurag Nema Rishabh Rejput	EE 3rd year	Brishalt
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(**Dr. Ranjeet Kumar Singh**) Workshop Coordinator

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(**Dr. Kalka Dubey**) Workshop Coordinator



(**Dr. Manish Dixit**) Professor & Head