



# MADHAV INSTITUTE OF TECHNOLOGY & SCIENCE, GWALIOR (M.P.), INDIA

## माधव प्रौद्योगिकी एवं विज्ञान संस्थान, ग्वालियर (म.प्र.), भारत

A GOVT. AIDED UGC AUTONOMOUS & NAAC ACCREDITED INSTITUTE, AFFILIATED TO R.G.P.V BHOPAL (M.P)

### Department of Electrical Engineering

#### Analog Electronics Lab (130304)

##### LIST OF EXPERIMENTS

1. To measure and plot the forward and reverse V-I characteristics of diode
2. To measure and plot the forward and reverse V-I characteristics of the SCR
3. To determine ripple factor and rectification efficiency of half wave rectifier with and without filter
4. To determine ripple factor and rectification efficiency of full wave rectifier with and without filter
5. To test and plot input and output common emitter transistor characteristics
6. To verify the operation of Darlington pair and also determine the gain, input and output impedances
7. To construct Wien bridge oscillator using 741 op-amp and to measure the frequency of oscillation
8. To design and test differential Amplifier using Transistor
9. To determine and verify the amplification and voltage gain of a two stage RC coupled Amplifier
10. To construct push pull Amplifier and calculate the efficiency

SCAN or Click Links given below



[Playlist-01: Click Here](#)

[Playlist-02- Click Here](#)



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### Department of Electrical Engineering

#### Digital Electronics Lab (130401)

##### LIST OF EXPERIMENTS

1. Verification of truth tables of (a)OR, AND, NOT gates (By using 7400 series) , (b)NAND & NOR gates and (c)EX-NOR & EX-OR gates
2. Verification of De-Morgan's Theorem using ICs
3. Implementations of Multiplexer & Demultiplexer using logic gates (ICs) and verify truth table
4. Implementations of Encoder & Decoder using logic gates (ICs) and verify truth table
5. Implementations of Half Adder & Full Adder using logic gates (ICs) and verify truth table
6. Implementations of Half Subtractor & Full Subtractor using logic gates (ICs) and verify truth table
7. Implementation of Binary to Grey Code & Excess- 3 to BCD Converter using logic gates
8. Operation and verifying truth tables of flip- flops- RS, D, and JK using ICs
9. To perform addition & subtraction of two 8-bit numbers using 8085
10. To perform the multiplication & division of two 8-bit numbers using 8085

SCAN or Click Links given below



[Playlist-01: Click Here](#)

[Playlist-02- Click Here](#)