

MADHAV INSTITUTE OF TECHNOLOGY & SCIENCE, GWALIOR

(A Govt. Aided UGC Autonomous Institute, Affiliated to RGPV, Bhopal)

NAAC Accredited with A++ Grade

Department of Electronics Engineering

Report of Webinar

Date: 09.02.2024

Name of Activity: Webinar on “**Campus to Corporate in VLSI Design**”.

Date of Activity: 09.02.2024

Name of Coordinator: Dr. Vikas Mahor.

Number of participants attended the online webinar: 47

Name of Expert : Ms. Apurva Gupta, Senior VLSI Engineer, Microsoft, India

Introduction:

The one-day online workshop on "Campus to Corporate on VLSI Design" was organized to bridge the gap between academic knowledge and industry requirements in the field of Very Large Scale Integration (VLSI) design. The workshop aimed to provide valuable insights and guidance to students transitioning from campus to corporate roles in VLSI design. It was conducted by a coordinator and an expert, a senior VLSI engineer from Microsoft.

Workshop Agenda:

The workshop commenced with an introduction to the objectives and agenda by the coordinator, emphasizing the importance of understanding industry expectations and acquiring relevant skills for a successful transition from campus to corporate roles in VLSI design.

Session 1: Introduction to VLSI Design:

The expert provided an overview of VLSI design, covering fundamental concepts, methodologies, and tools used in the industry. They highlighted the significance of VLSI technology in various electronic devices and systems, setting the stage for the subsequent sessions.

Session 2: Industry Expectations and Skill Development:

In this session, the expert shared insights into the skills and competencies expected from VLSI engineers in the corporate world. They discussed the importance of continuous learning, problem-solving abilities, and effective communication skills in a professional environment. Additionally, the expert provided guidance on enhancing technical proficiency through practical projects and certifications.

Session 3: Career Opportunities and Growth in VLSI Design:

The expert elaborated on the diverse career opportunities available in the field of VLSI design, ranging from chip design and verification to system-level integration and architecture. They also discussed potential career paths and growth trajectories for VLSI engineers, emphasizing the importance of networking, mentorship, and continuous career development.

Session 4: Insights from Industry Experience:

Drawing from their extensive experience in the industry, the expert shared real-world examples, case studies, and best practices in VLSI design. They provided valuable insights into industry trends, challenges, and emerging technologies, offering practical advice to students on how to navigate the corporate landscape effectively.

Interactive Q&A Session:

The workshop concluded with an interactive Q&A session, allowing participants to engage with the expert and seek clarification on various topics discussed throughout the day. Participants had the opportunity to ask questions related to career planning, skill development, industry trends, and specific challenges faced by VLSI engineers.

Key Takeaways:

Understanding of fundamental concepts and methodologies in VLSI design.

Awareness of industry expectations and skill requirements for corporate roles.

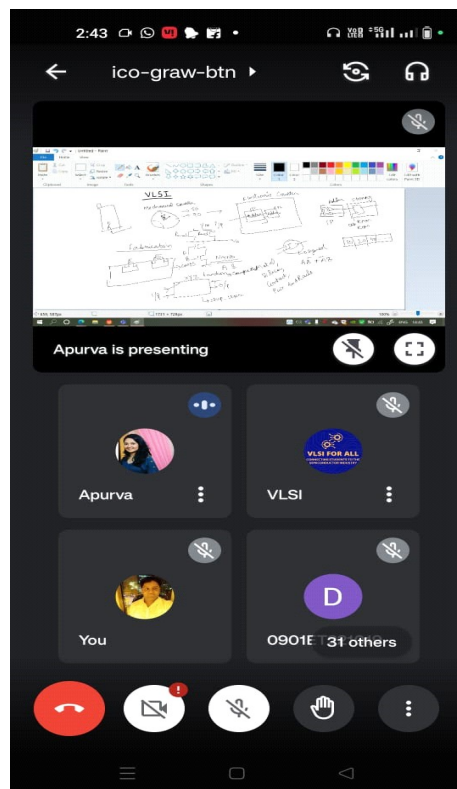
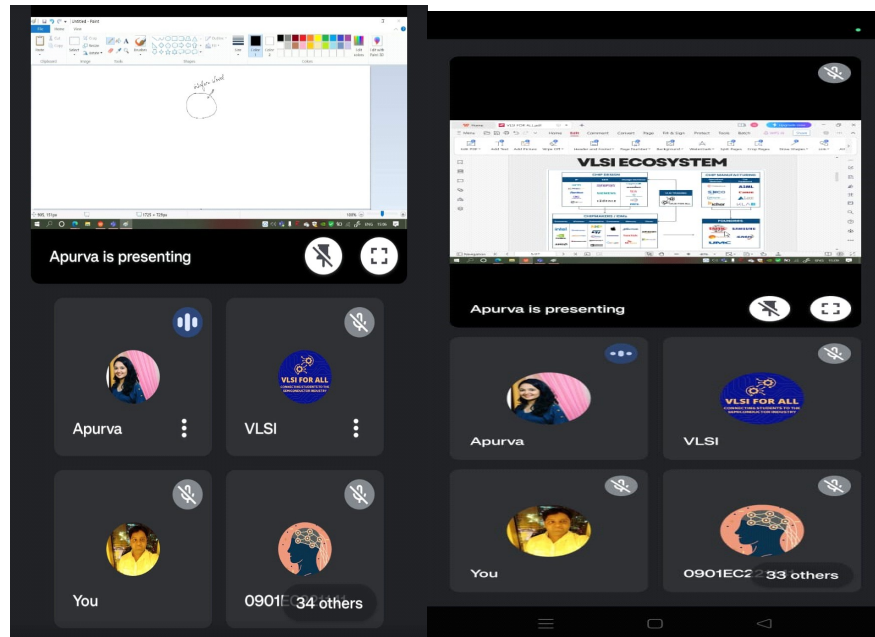
Insights into career opportunities, growth prospects, and industry trends in VLSI design.

Practical advice and guidance from industry experts on navigating the transition from campus to corporate roles.

Conclusion:

The one-day online workshop on "Campus to Corporate on VLSI Design" provided valuable insights, guidance, and practical advice to students aspiring to pursue careers in VLSI design. The expertise shared by the senior VLSI engineer from Microsoft, coupled with interactive discussions and Q&A sessions, contributed to a enriching learning experience for all participants. By bridging the gap between academia and industry, the workshop empowered students to make informed career choices and embark on successful journeys in the field of VLSI design.

Snapshots of the Webinar:



Vikas Mahor
(Dr. Vikas Mahor)
Coordinator