



माधव प्रौद्योगिकी एवं विज्ञान संस्थान, ग्वालियर (म.प्र.), भारत Madhav Institute of Technology & Science, Gwalior (M.P.), INDIA

Deemed University

(Declared under Distinct Category by Ministry of Education, Government of India)

NAAC ACCREDITED WITH A++ GRADE



EDITORIAL BOARD:

1. Faculty Members:

- Dr. Vandana Vikas Thakare
(H.O.D.)
- Dr. Shubhi Kansal
(Faculty Coordinator)

2. Student Members:

- Satyam Mishra
(EC - 4th year)
- Sanskar Mishra
(EC - 4th year)

Contact:

electronicsnewsletter@gmail.com

**DEPARTMENT OF ELECTRONICS ENGINEERING
E-NEWSLETTER
January - March 2025**



माधव प्रौद्योगिकी एवं विज्ञान संस्थान, ग्वालियर (म.प्र.), भारत
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Department Vision

“ To Prepare Globally Competent Electronics Engineers for Industrial Challenges and Societal needs. ”

Department Mission

- To provide quality education through need based curriculum and effective teaching learning process.
- To imbibe professional ethics, leadership and entrepreneur skills with a passion for lifelong learning.
- Strengthening of employability skills through multidisciplinary projects and internships.
- To inculcate the spirit of innovation and research to serve the needs of the society and industry.

DEPARTMENT OF ELECTRONICS ENGINEERING



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PROGRAMME EDUCATIONAL OBJECTIVES (PEOs)

PEO-1

Graduates of the program will be successful global collaborators with thriving technical and professional careers in the field of Electronics Engineering.

PEO-2

Graduates will have the ability to adapt latest technologies to contribute for sustainable development of society with effective research and entrepreneurship attitude.

PEO-3

Graduates will have the teamwork, professional excellence, communication, and interpersonal skills to enable them to work effectively with interdisciplinary teams in Industry, Government, and Academia.

DEPARTMENT OF ELECTRONICS ENGINEERING



JANUARY - MARCH 2025

CONTENTS:

1. FDP/STC attended (Outside/Inside) the Institute
2. Expert talk given (Outside/Inside) the Institute
3. NPTEL/ATAL/Internshala courses attended
4. Publications SCI/Scopus/UGC with DOI Number
5. Student Club Activities
6. Departmental Activity
7. Student Achievements





FDP/STC ATTENDED (OUTSIDE/INSIDE) THE INSTITUTE

Dr. R.P Narwaria

- Two Day Online National Workshop on “Modelling & Simulation using MATLAB Software” in association with Ark InfoSolution [MathsWork], Madhya Pradesh on 23rd & 24th January 2025

Dr. Varun Mishra

- Faculty Development Program on “Nanotechnology for VLSI: Fabrication and Challenges” jointly organized by Electronics and ICT Academy, NIT Patna and Malla Reddy College of Engineering and Technology, Hyderabad, Telangana under the “Scheme of financial assistance for setting up of Electronics and ICT Academies (Phase-II)” by the Ministry of Electronics and Information Technology (MeitY), Government of India from 03rd February– 07th February, 2025.

Prof. Prateek Bhadauria

- FDP - AI/ML Applications for Next-Generation Wireless Communication held on 3-9th February, 2025 and organized by Department of Electrical and Electronics Engineering, Atal Bihari Vajpayee-Indian Institute of Information Technology and Management, Gwalior, India

Dr. Jaydeep Parmar

- One week Faculty Development Program on “Nanotechnology for VLSI: Fabrication and Challenges” jointly organized by Electronics and ICT Academy, NIT Patna, and Malla Reddy College of Engineering and Technology, Hyderabad, Telangana, under the “Scheme of financial assistance for setting up of Electronics and ICT Academies (Phase-II)” by the Ministry of Electronics and Information Technology (MeitY), Government of India from 03rd February– 07th February, 2025.



FDP/STC ATTENDED (OUTSIDE/INSIDE) THE INSTITUTE

Dr. Himanshu Singh

- Participated & successfully completed a 10-days FDP on “IoT and Machine Learning for Advanced Biomedical Signal Processing” held from 15 Jan to 24 Jan 2025, at ABV-Indian Institute of Information Technology and Management, Gwalior.

Dr. Shubhi kansal

- Participated & successfully completed a one week FDP on “ Applied AI- Practical Implementations” held from 3-7 Feb 2025 under TechSaksham Program.



EXPERT TALK GIVEN (OUTSIDE/INSIDE) THE INSTITUTE

Dr. Jaydeep Parmar

- Artificial Intelligence (AI) in Healthcare on 13th January 2025, by Dr. Sidharth Pancholi from Mphasis, India.

Dr. Himanshu Singh

- Organized an IETE-sponsored Expert Talk by Dr. Ravi Yadav on "Next-Generation Electromagnetic Techniques for Absorber Design" at MITS Gwalior on January 11, 2025.
- Organized an IETE-sponsored Expert Talk by Dr. Meha Agrawal on "Emerging Antenna Technology: Fractional Substrate Integrated Waveguide Solutions for Size Miniaturization" at MITS Gwalior on January 12, 2025.



NPTEL/ATAL/INTERNSHALA COURSES ATTENDED

Dr. Karuna Markam got the certificate of appreciation for the NPTEL Believer (July-Dec 2024)



FACULTY AWARDS & RECOGNITIONS





PUBLICATIONS SCI/SCOPUS/UGC WITH DOI NUMBER

Dr. Vandana Vikas Thakare

Rachit jain, R. Ramya, Vandana Vikas Thakare and PK Singhal, “Design and analysis of antenna through machine learning for next-generation IoT system”, Discover Internet of Things, March 2025.



PUBLICATIONS SCI/SCOPUS/UGC WITH DOI NUMBER

Dr. Varun Mishra

- 1. Yogesh Kumar Verma, Varun Mishra, Manoj Singh Adhikari, Suman Lata Tripathi, and Santosh Kumar Gupta. "Comparative Analysis of AlN/ β -Ga₂O₃ and MgZnO/CdZnO Gate All Around Field Effect Transistors (GAA-FETs) for Analog/RF Applications." Journal of Circuits, Systems and Computers (2024): 2550125.
- 2. Lucky Agarwal, Shreyas V. Devadiga, Swati Dixit, and Varun Mishra. "Design & Implementation of Nano Cavity TFET Based Smart Sensor for Water Quality." Sensing and Imaging 26, no. 1 (2025): 8.

Dr. Deep Kishore Parsediya

- D. K. PARSEDIYA ,A. S. Sengar and P. Bhardwaj, "Performance Evaluation of Different Antenna Shapes (Hexagonal, Pentagonal, Circular, Octagonal Triangular) for Sub-6 GHz Band in 5G Communications," 2025 IEEE International Students' Conference on Electrical, Electronics and Computer Science (SCEECS), Bhopal, India, 2025, pp. 1-6, doi: 10.1109/SCEECS64059.2025.10940521.

R. Jenkin Suji

- Atharva Shrotriya, Mahim Dixit, R. Jenkin Suji, Graphical User Interface Tool for the Development of Machine Learning Models, 3RD INTERNATIONAL STUDENT CONFERENCE ON MULTIDISCIPLINARY AND CURRENT TECHNICAL RESEARCH - 2025

- **Dr. Shubhi kansal**

Shubhi kansal, Unsharp masking and histogram equalization for remote sensing image enhancement, SCOPE journal, Scopus, March 2025.



PUBLICATIONS SCI/SCOPUS/UGC WITH DOI NUMBER

Prof. Pooja Sahu

- Shrivastav, Prasann, and Pooja Sahoo. "Hydraulic solar tracker: Solar energy enhancer with hydraulic tracking system." AIP Conference Proceedings. Vol. 3162. No. 1. AIP Publishing, 2025. 2- Harsh Magarde¹, Pooja Sahoo², Rachit Jain³, Suryansh Rajawat⁴, Khan Anas Shokat⁵ Arc-Shaped Planar Monopole Antenna for Next-Generation 5G Communication" at "2025 3rd International Conference on Communication, Security, and Artificial Intelligence", which will be held at Galgotias University, Greater Noida, India (203201), from April 4-6, 2025.

Dr. Himanshu Singh

- H. Gupta, H.Singh, and A.Kumar, " RAOMIFD: RPi Assisted Optimal Mode-Based Image-Denoising Framework Using Diffusion-Filter ", in IEEE Transactions on Consumer Electronics, doi: 10.1109/TCE.2025.3546809 (2025). <https://ieeexplore.ieee.org/abstract/document/10912494> Optimized Route Planning and Dynamic Fare Estimation for Multimodal Urban Transit Networks Using Dijkstra's Algorithm Priyansh Trivedi and Himanshu Singh in 3rd International Student Conference on Multidisciplinary and Current Technical Research (ISCMCTR - 2025)

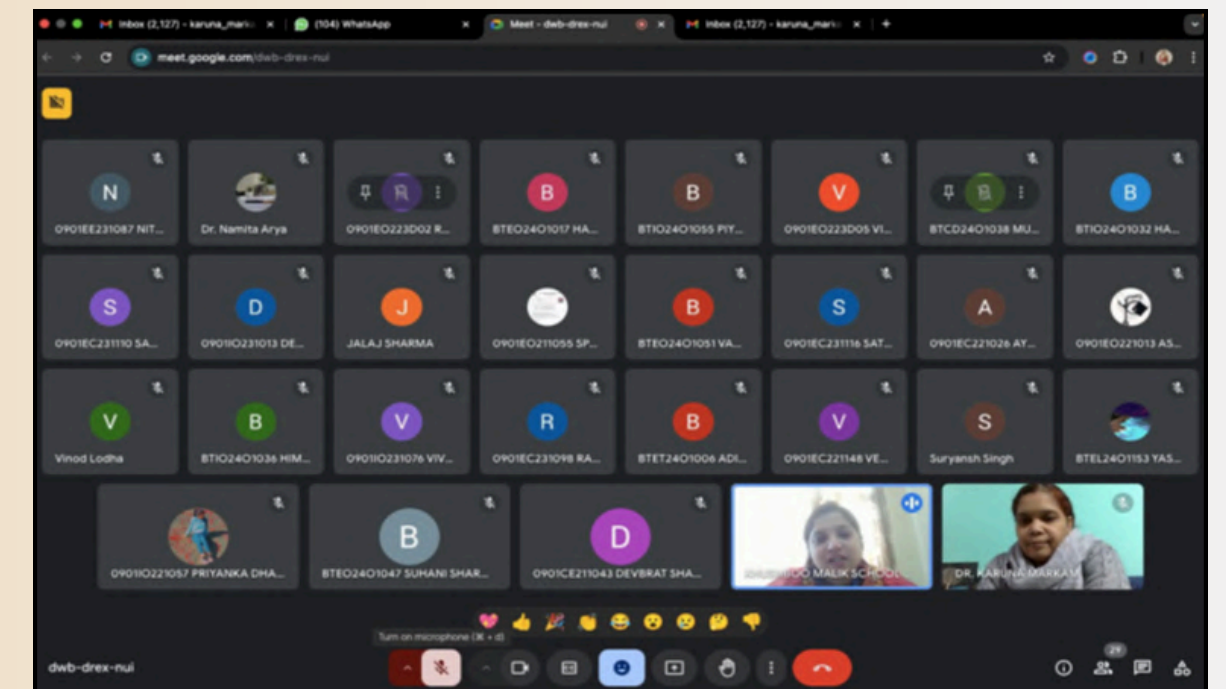
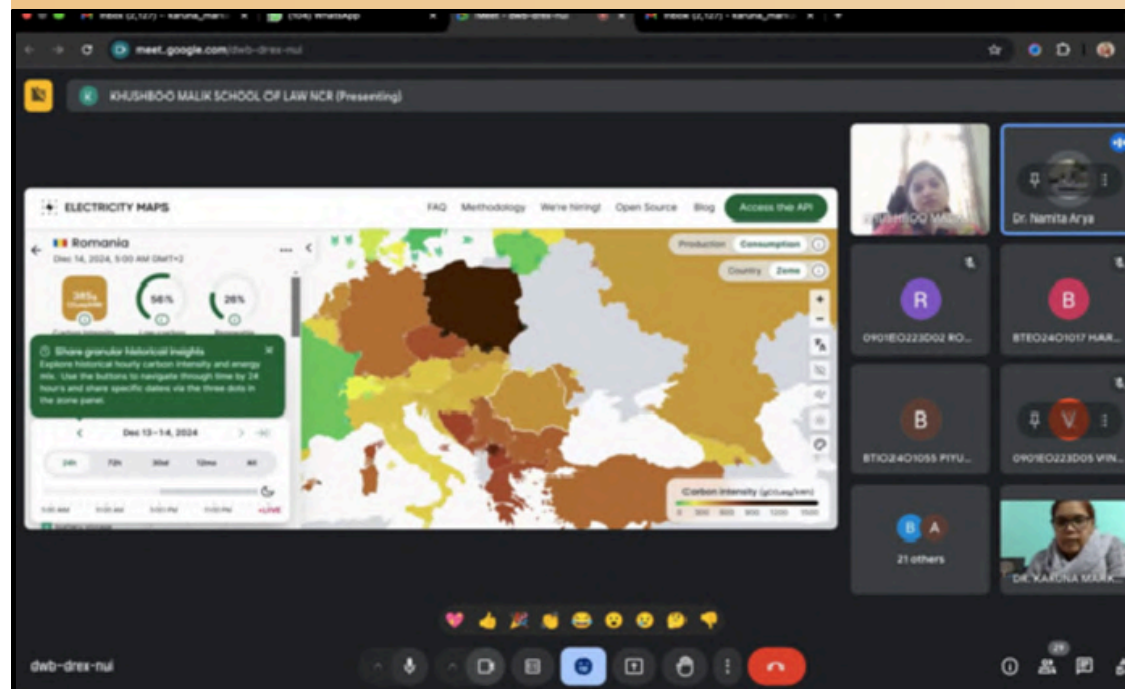
Dr. Karuna Markam

- V. Gurjar, P. Singh, K. Markam, S. S. Tomar and V. Agrawal, "Third Eye," 2024 IEEE 2nd International Conference on Innovations in High Speed Communication and Signal Processing (IHCSP), Bhopal, India, 2024, pp. 1-7, doi: 10.1109/IHCSP63227.2024.1096000



STUDENT CLUB ACTIVITIES

- Online Panel Discussion and Interaction of Alumni - Students on “Industrial Demand and Current Adopted Technologies in the Field of Electronics-III” in association with IETE Bhopal centre.
- Shame-ulfat organized by Hindi Samiti on 15th Feb 2025
- Yuva Mahotsav organized by Hindi Samiti on 12 January 2025.



DEPARTMENT OF ELECTRONICS ENGINEERING



एमआईटीएस के छात्र-छात्राओं ने स्वामी विवेकानंद के मार्ग पर चलने का लिया संकल्प

सत्ता सुधार ■ ग्वालियर

माधव प्रौद्योगिकी एवं विज्ञान संस्थान-सम विश्वविद्यालय में स्वामी विवेकानंद जयंती पर छात्रों में स्वामी विवेकानंद के आदर्शों और उनके विचारों को प्रोत्साहित करने हेतु संस्थान की हिंदी समिति और फिटनेस क्लब द्वारा संयुक्त रूप से युवा उत्सव का आयोजन किया। इस आयोजन में सैकड़ों छात्रों ने भाग लिया और एक विशेष प्रेरक सत्र आयोजित किया, जिसमें फिर एक प्रयास संस्था के संस्थापक पं. अंकित शर्मा ने स्वामी विवेकानंद और युवा शक्ति विषय पर छात्रों को सम्बोधित किया। उन्होंने छात्रों को स्वामी विवेकानंद के जीवन के प्रमुख पहलुओं, उनके संघर्ष, समर्पण और राष्ट्र निर्माण में योगदान से अवगत कराया। साथ ही



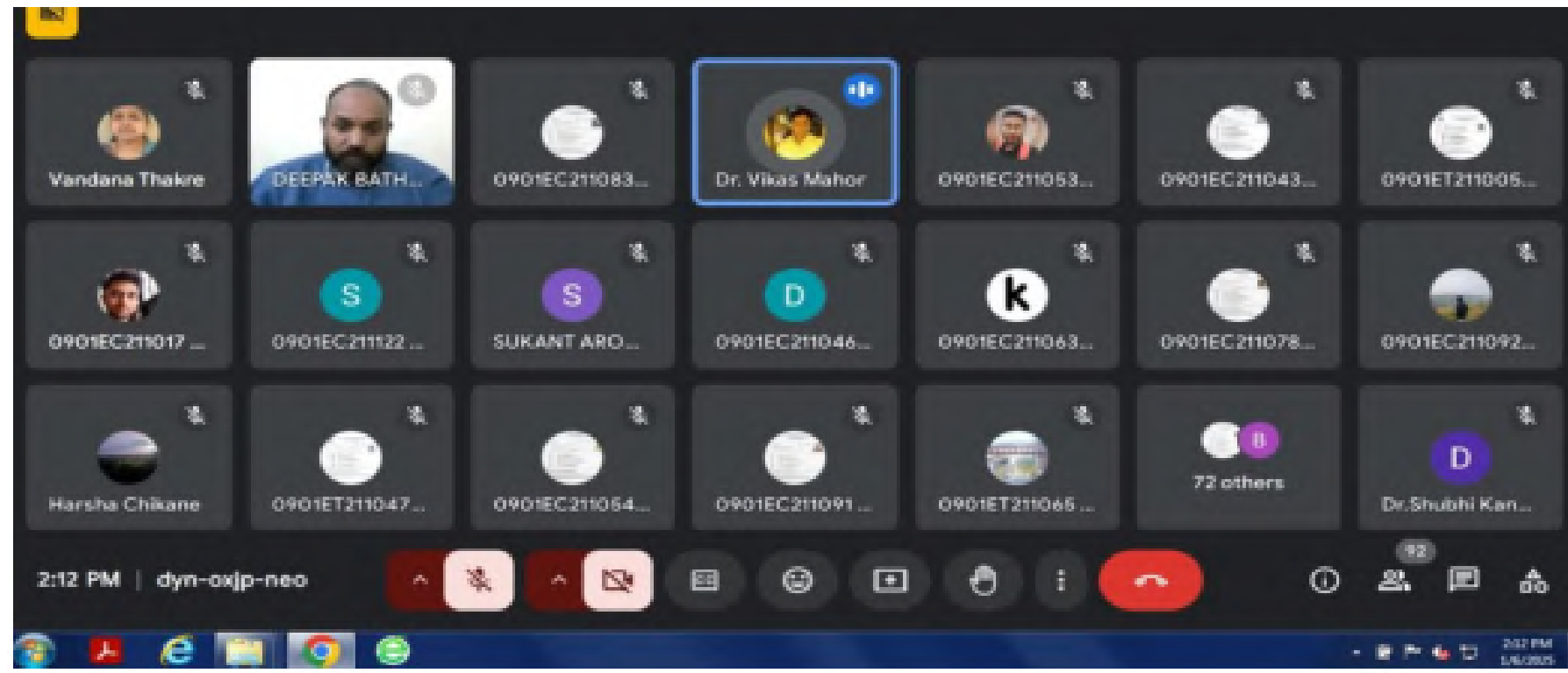
छात्रों में शारीरिक और मानसिक स्वास्थ्य के प्रति जागरूकता को बढ़ाने हेतु सूर्य नमस्कार सत्र भी आयोजित किया गया। इस सत्र का मार्गदर्शन एलएनआईपीई के योग शोधार्थी सुश्री पूजा सिंह ने किया एवं सूर्य नमस्कार के लाभों के बारे में विस्तार से बताया। उन्होंने बताया कि सूर्य नमस्कार न केवल शरीर को स्वस्थ बनाता है, बल्कि मानसिक शांति भी प्रदान करता है। इस

अवसर पर मौजूद छात्रों ने योग को अपनी दैनिक दिनचर्या में शामिल करने का संकल्प लिया। इस अवसर पर विश्वविद्यालय की हिन्दी समिति की समन्वयक डॉ. शुभी कंसल उपस्थित थीं। कार्यक्रम की भूमिका नीतेश गुर्जर ने रखी एवं समिति के अध्यक्ष पुष्पेन्द्र सिंह गुर्जर ने आभार ज्ञापन किया। फिटनेस क्लब की अध्यक्ष माही शुक्ला ने अतिथियों का स्वागत किया।



DEPARTMENTAL ACTIVITY

- Orientation program for first year was organized on 3 Feb 2025
- Orientation for 2, 3 and 4 year was organized on 6th January 29=2025

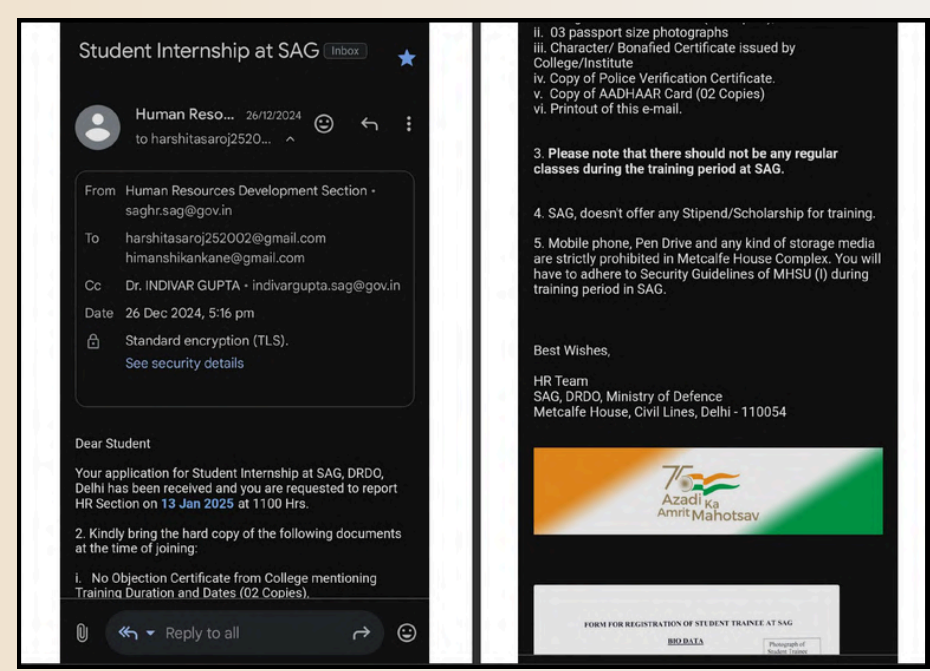
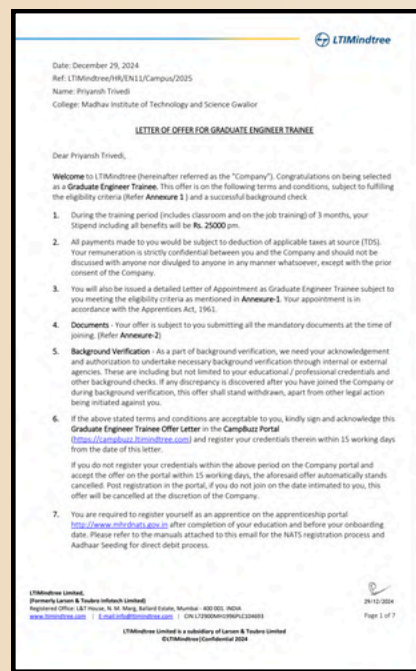
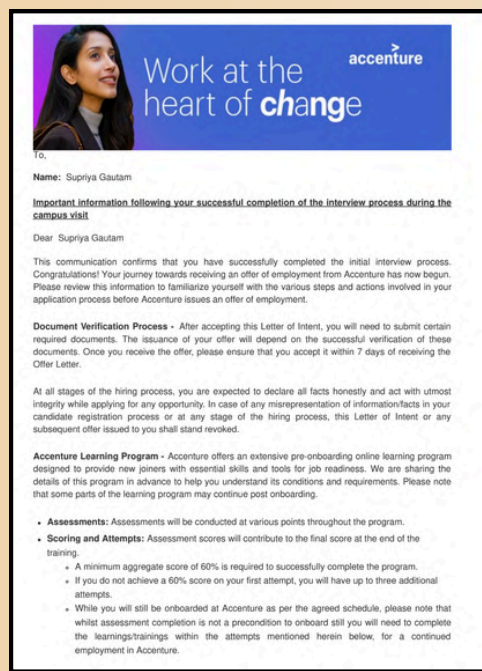
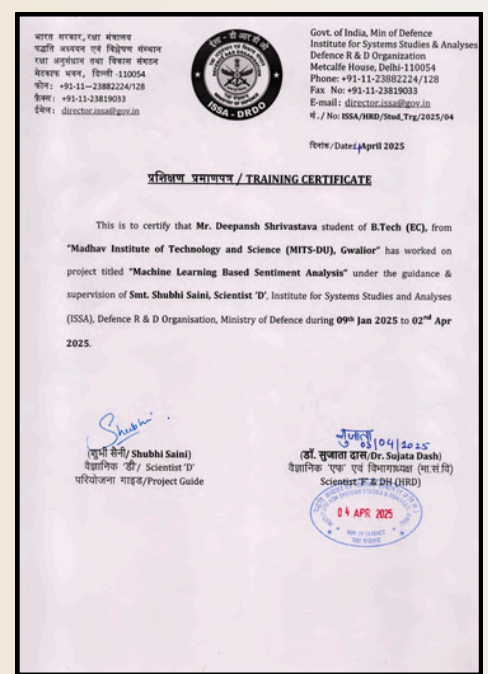
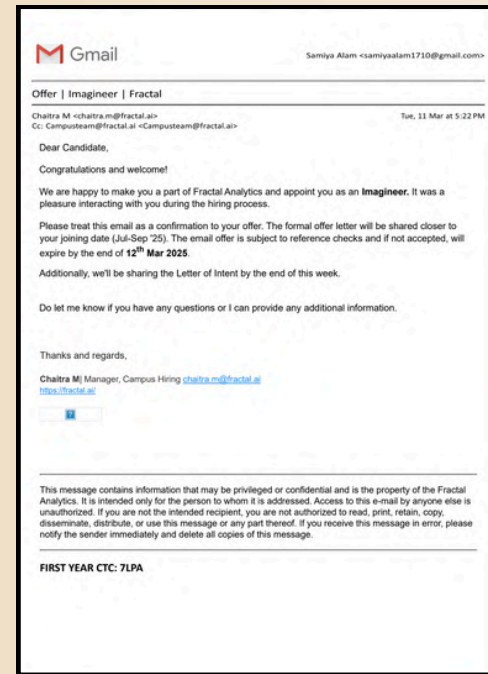
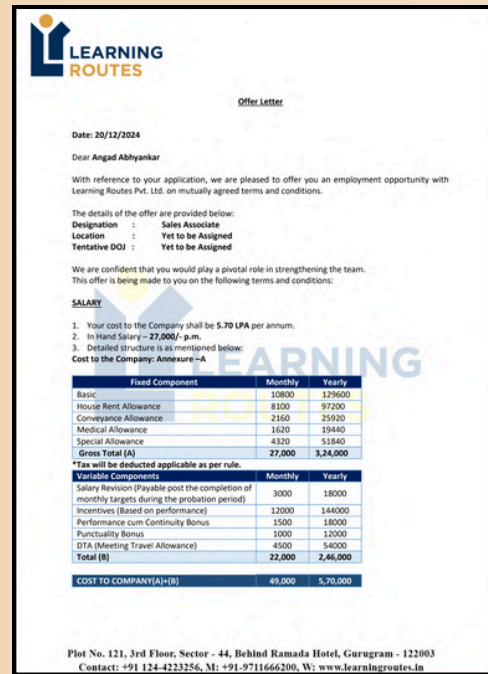




DEPARTMENT OF ELECTRONICS ENGINEERING



STUDENT ACHIEVEMENTS



DEPARTMENT OF ELECTRONICS ENGINEERING





Lateral Hire (LOI)

Superscript 01: 5168693

Date: December 13, 2024

Dear **WIMBURY KARKAT**,

Our executives have recently selected a highly talented individual for our Cognizant Family.

1. This LOI refers to your application for employment with Cognizant Technology Solutions India Private Limited ("Cognizant") and subsequent disclosures. Based on the information and representation provided by you, we have taken your application for **GenC**, and your **discovery** and **Programmer Analyst Trainee**. Upon final selection, you will receive a formal offer letter for employment. Offer will include compensation, terms and conditions, work regulations & policies ("Employment Agreement") will be issued and such Employment Agreement shall be subject to and effective only upon your acceptance of Cognizant employment verification.

2. Full time interview is opportunity to:

• You will have an opportunity to attend a full time interview where you will be subjected into a formal training in a business specific skill, and it will be used as a basis to select your assignment and terms and conditions governing such training program will be shared in the Offer Letter.

• You will be evaluated for the position of this full time interview program and based on the results we will confirm you to be an employee of Cognizant or the scope of this full time interview program does not include any representation responsibilities and that it is not a basis for employment verification.

• Cognizant shall only verify your worker's compensation information or any health or accident insurance under this full time interview program and not be liable to pay any contributions to applicable statutory policies such as worker's compensation, provident fund and any other contribution or benefits which would be expected in an employee employment agreement.

• Cognizant will not encourage any deals with regard to compensation or other statutory payments under this LOI and it is hereby clarified that this is full-time interview program shall not entitle you for any benefits and/or made available to that of Cognizant employees.

• Cognizant's completion of full-time interview program and completion of background verification, you would be onboarded as a full-time employee under a full-time Employment Agreement.

• A direct full-time employment is expected to you.

• Upon your successful completion of background verification, you would be onboarded as a full-time employee under a definitive employment Agreement, where you should successfully complete the Cognizant Certified training program. In the event of unsatisfactory performance during this training, Cognizant reserves rights to at its sole discretion to rehire this LOI and full time employee under a full-time Employment Agreement.




• Upon successful completion of full-time interview and joining as full time employee or directly joining as full time employee, your annual compensation will be **INR 4.38,280/- per annum**. The standard information on compensation and benefits will be provided in the full-time employment Offer Letter.

• Cognizant has your personal bank account information in your file and reserves all rights to request for tracing records and issue of full-time employment is a matter of internal record and not constitute in any contractual relationship between you and Cognizant. Cognizant holds no rights to withdraw or cancel this LOI and/or the full-time employment Offer Letter due to non-compliance of background checks or more people selected.

Code of Conduct: You shall comply with Cognizant Code Values and Standards of Business conduct and accept and incorporate the same in your employment.

• This LOI/On Hire Agreement is valid for **7 calendar days**. From the date of this LOI, Hence, you are requested to accept or decline the LOI within 7 calendar days from the date of this LOI - in case we do not receive any response from you and within the aforementioned 7 calendar days, this LOI shall stand automatically terminated. You are requested to kindly let us be of the use discussed. Please ensure that the LOI is not deemed to be considered as an employment contract or offer for other purposes of joining Cognizant as an employee. For an employment relationship, the Additional Employment Agreement shall be signed.

• Upon accepting this LOI, you hereby consent that Cognizant Technology Solutions India Private Limited to employ the personal services of the selected personal employee and shall be responsible for your background verification and ensure that the same with an empowered personnel is made for conducting mandatory background checks for employment opportunities with Cognizant.

<p>BARC Training School  Mumbai - 400 094</p>	<p>Fax Number: 91-22-559698 Email: hrd@barc.gov.in</p>
<p>Government of India Bhabha Atomic Research Centre Radiation Resource Development Division</p>	
	
<p>Ref: HRD/2024/24118 Head Mahav Institute of Technology & Science Coimbatore</p>	
Date: December-6, 2024	
<p>Subject: Practical Training Test for the students of your institution at BARC.</p>	
<p>The application of Mr. Tarang Nal. Tripathi, Electronics Engineering student of your institute has been accepted for practical training project work at RDD, BARC under the guidance of Shri Tushar Suman from <u>12-Jan-25</u> to <u>12-Feb-25</u>.</p>	
<p>Guidelines for the students reporting for Practical Training Project Work in BARC.</p>	
<p>Students are advised to submit following documents at BARC Training School, Mumbai not later than 15 days of starting date of training:</p>	
<p>1. Bio-data form with photograph and Information Sheet should be signed by the Principal/Head of the institution with a stamp. The same signature and seal should be across the photograph.</p>	
<p>2. Photograph of Candidate Identity Card and address to be submitted by the Principal/Head of the institution with signature and official stamp of the institution. The photograph and information is according to the copy and attach it with signature.</p>	
<p>3. Undertaking, Declaration of Undertaking and Declaration forms should be printed on plain paper and fully fill it.</p>	
<p>4. A non-judicial stamp paper of your format of Rs. 100/- or higher denomination should be submitted. Non-Judicial stamp paper should be not dated earlier than 180 days from the date of reporting.</p>	
<p>5. Guaranteed Officer's Certificate/Performance Certificate should be on letter head or on plain format.</p>	
<p>6. One copy of the recent (from last) photograph-4x6 cm. Sign is required.</p>	
<p>7. Student has to pay the amount of ₹ 200/- when they report to BARC which can be debit card, credit card or a Demand Draft of Nationalized Bank in favour of "Accounts Officer BARC payable Mumbai. (Estate value of ID should be more than one month after submission to 1-Card only). (NOT APPLICABLE FOR STUDENT INTERNS).</p>	
<p>8. Photograph, Declaration, Temporary Entry Permit and demand draft are to be submitted at Training School. These documents are required for securing the form of identity card.</p>	
<p>9. Check list is for guiding and sequencing of documents for submission.</p>	
<p>10. Only the following documents in sealed envelope of the form of identity card in Duplex kept at Training School, ground floor of BARC Training School, Near HRDD entrance, Annabhamburda, Mumbai.</p>	
<p>11. After sealed envelope, Introduction letter will be issued at Computer Lab no. 11, "BARC Training School, Entrance No. 11, 11.55 hrs. on Tuesday and Wednesday, on the day of locality, on the day of ID card, it will be issued on the next working day.</p>	
<p>12. BARC ID card is secured area, carrying of any electronic items such as mobile phone, sim card, ear phones, ear pods, digital watches, pen drive, electronic/digital media, social media, android/cassettes, camera, USB drives or any other items are STRICTLY PROHIBITED under BARC premises.</p>	
<p>13. BARC ID card is very sensitive document, carrying of anything, employing on public facilities is an offensive act. Student has to respect corporate identity policy always wearing displaying BARC ID card.</p>	
<p>14. Completion of training, students are to be issued a certificate of training by the North Gurgaon, BARC.</p>	
<p>15. Student always has to carry corporate identity card of own college/institution in BARC.</p>	
<p>16. The students' period for the training project work should be not exceed during training.</p>	
<p>ACCOMMODATION WILL NOT BE PROVIDED BY BARC DURING THE TRAINING PERIOD.</p>	
<p>THE STUDENTS HAVE TO MAKE THEIR OWN ARRANGEMENTS FOR THE STAY IN MUMBAI.</p>	
<p>Thanking You,</p>	<p>Yours faithfully,  Dr. Vanda Yalmali Project Manager Practical Training & Project Work</p>



STUDENT ACHIEVEMENTS

GRADUATE APTITUDE TEST IN ENGINEERING 2025 अभियांत्रिकी कक्षा अभियुक्त परीक्षा २०२५ Organising Institute: INDIAN INSTITUTE OF TECHNOLOGY ROORKEE	
SCORE CARD	
Name of the Candidate	HIMANSHI KANKANE
Name of the Parent/Guardian	RAKESH KANKANE
Registration No.	EC25565015310
Date of Birth	December 22, 2002
Test Paper	Electronics and Communication Engineering (EC)
Date of Examination	February 15, 2025
GATE Score	350
Marks out of 100	25.0
All India Rank (AIR) in the test paper:	10284
Number of candidates appeared for the test paper:	81475
Qualifying Marks	General: 25.0 EWS/OBC-NCL: 22.5 SC/ST/PwD: 16.6
A candidate is considered qualified if the marks scored are greater than or equal to the qualifying marks mentioned for the category, for which a valid category certificate, if applicable, must be produced along with this Score Card.	
This Score Card is valid up to 31 st March 2026.	
GATE SCORE COMPUTATION The GATE 2025 score is calculated using the formula: $\text{GATE Score} = S_u + (S_t - S_u) \frac{(M - M_u)}{(M_u - M_u)}$ where, M is the marks obtained by the candidate in the test paper mentioned on the GATE 2025 Score Card M _u is the qualifying marks for general category candidates in the test paper M _u is the mean of marks of top 0.1% or top 10 (whichever is larger) of all the candidates who appeared in the test paper S _u = 350, is the score assigned to M _u and S _t = 900, is the score assigned to M _t In the GATE 2025 score formula, the qualifying marks (M _u) for the general category candidate in each subject will be : Cut-off marks for GENERAL category = max(25, min(40, $\mu + 6\sigma$)). Here μ is the mean and σ is the standard deviation of positive marks of all the candidates who appeared in the test paper. Qualifying in GATE 2025 does not guarantee admission to a postgraduate program or scholarship/financial assistance. Admitting institutes may conduct additional tests or interviews for final selection of candidates. Graduate Aptitude Test in Engineering (GATE) 2025 was organised by Indian Institute of Technology Roorkee on behalf of National Coordination Board (NCB) - GATE for the Department of Higher Education, Ministry of Education (MHE), Government of India.	

GRADUATE APTITUDE TEST IN ENGINEERING 2025 अभियांत्रिकी कक्षा अभियुक्त परीक्षा २०२५ Organising Institute: INDIAN INSTITUTE OF TECHNOLOGY ROORKEE	
SCORE CARD	
Name of the Candidate	SUHAN JAIN
Name of the Parent/Guardian	KAMAL JAIN
Registration No.	CS25515015365
Date of Birth	December 2, 2003
Test Paper	Computer Science and Information Technology (CS)
Date of Examination	February 15, 2025
GATE Score	324
Marks out of 100	26.64
All India Rank (AIR) in the test paper:	26769
Number of candidates appeared for the test paper:	170825
Qualifying Marks	General: 29.2 EWS/OBC-NCL: 26.2 SC/ST/PwD: 19.4
A candidate is considered qualified if the marks scored are greater than or equal to the qualifying marks mentioned for the category, for which a valid category certificate, if applicable, must be produced along with this Score Card.	
This Score Card is valid up to 31 st March 2026.	
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GRADUATE APTITUDE TEST IN ENGINEERING 2025 अभियांत्रिकी कक्षा अभियुक्त परीक्षा २०२५ Organising Institute: INDIAN INSTITUTE OF TECHNOLOGY ROORKEE	
SCORE CARD	
Name of the Candidate	ANKIT YADAV
Name of the Parent/Guardian	RAMNATH YADAV
Registration No.	IN25525015064
Date of Birth	September 6, 2003
Test Paper	Instrumentation Engineering (IN)
Date of Examination	February 15, 2025
GATE Score	357
Marks out of 100	26.0
All India Rank (AIR) in the test paper:	1808
Number of candidates appeared for the test paper:	11935
Qualifying Marks	General: 25.0 EWS/OBC-NCL: 23.0 SC/ST/PwD: 17.0
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SCORE CARD	
Name of the Candidate	HARSH GOYAL
Name of the Parent/Guardian	NARESH GOYAL
Registration No.	EC25565015227
Date of Birth	May 27, 2001
Test Paper	Electronics and Communication Engineering (EC)
Date of Examination	February 15, 2025
GATE Score	654
Marks out of 100	48.67
All India Rank (AIR) in the test paper:	1218
Number of candidates appeared for the test paper:	81475
Qualifying Marks	General: 25.0 EWS/OBC-NCL: 22.5 SC/ST/PwD: 16.6
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SCORE CARD	
Name of the Candidate	ANKITA YADAV
Name of the Parent/Guardian	VINOD KUMAR YADAV
Registration No.	EC25565015309
Date of Birth	December 6, 2003
Test Paper	Electronics and Communication Engineering (EC)
Date of Examination	February 15, 2025
GATE Score	377
Marks out of 100	27.33
All India Rank (AIR) in the test paper:	8305
Number of candidates appeared for the test paper:	81475
Qualifying Marks	General: 25.0 EWS/OBC-NCL: 22.5 SC/ST/PwD: 16.6
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SCORE CARD	
Name of the Candidate	ADITYA VERMA
Name of the Parent/Guardian	MANISH KUMAR VERMA
Registration No.	EC25565015254
Date of Birth	August 8, 2004
Test Paper	Electronics and Communication Engineering (EC)
Date of Examination	February 15, 2025
GATE Score	717
Marks out of 100	56.33
All India Rank (AIR) in the test paper:	371
Number of candidates appeared for the test paper:	81475
Qualifying Marks	General: 25.0 EWS/OBC-NCL: 22.5 SC/ST/PwD: 16.6
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GATE QUALIFIED STUDENTS

- HIMANSHI KANKANE
- SUHAN JAIN
- ANKIT YADAV
- HARSH GOYAL
- ANKITA YADAV
- ADITYA VERMA

AFCAT CLEARED STUDENT

- SANIKA KHARE

AFCAT 01/2025	
Candidate's Name :	Sanika Khare
Hallticket Number :	KA2502GLR171AA11156
Registration Number :	0125AA0011145
Date of Exam :	22 Feb 2025
Gender :	Female
Shift of Exam :	I
Exam	AFCAT
Marks scored	125
Normalised Marks	124.78
Cut Off Normalised Marks	121.00
Congratulations!!! You have successfully cleared first stage of AFCAT exam for all applied courses. Please click on below "AFSB Selection Process"	

DEPARTMENT OF ELECTRONICS ENGINEERING