(A Govt. Aided UGC Autonomous & NAAC Accredited Institute Affiliated to RGPV, Bhopal)

B.Tech. I Semester (Electronics Engineering)

Subject	Catego	Subject Name	Theory Slot				Total Mark	Contact Hr/week			Total Credit		
Code	ry						Mark	п	/we	eĸ	Credit		
	Code		End Mid		Quiz/	End	Lab	Skill	s	L	T	P	s
			Sem	Sem	Assignmen	Sem	work &	based					1
			Marks	Marks	t Marks	Mark	Sessiona	mini					
							l Mark	project					
2100022	BSC	Basic Electrical &	60	20	20	60	20	20	200	2	1	2	4
		Electronics Engineering											

Basic Electrical & Electronics Engineering (2100022)

Course Objectives:

- To impart the basic knowledge of the DC and AC circuits and their applications.
- To familiarize the students with the basic knowledge of magnetic circuits, transformer and its terminology.
- To make familiarize the students about the working of rotating electrical machine, various electronic circuits and its importance.

Unit I - D.C. Circuits Analysis:

Voltage and Current Sources: Dependent and independent source, Source conversion, Kirchhoff's Law, Mesh and Nodal analysis. Network theorems: Superposition theorem, Thevenin's theorem & Norton's theorem and their applications.

Unit II –Single-phase AC Circuits:

Generation of sinusoidal AC voltage, definitions: Average value, R.M.S. value, Form factor and Peak factor of AC quantity, Concept of Phasor, analysis of R-L, R-C, R-L-C Series and Parallel circuit, Power and importance of Power factor.

Unit III- Magnetic Circuits:

Basic definitions, AC excitation in magnetic circuits, self-inductance and mutual inductance, Induced voltage, laws of electromagnetic Induction, direction of induced E.M.F. Flux, MMF and their relation, analysis of magnetic circuits.

Unit IV- Single-phase Transformer & Rotating Electrical Machines:

Single phase transformer, Basic concepts, construction and working principal, Ideal Transformer and its phasor diagram at No Load, Voltage, current and impedance transformation, Equivalent circuits and its Phasor diagram, voltage regulation, losses and efficiency, testing of transformers, Construction & working principle of DC and AC machine.

Unit V - Digital Electronics, Devices & Circuits:

Number systems used in digital electronics, decimal, binary, octal, hexadecimal, their complements, operation and conversion, Demorgan"s theorem, Logic gates- symbolic representation and their truth table, Introduction to semiconductors, Diodes, V-I characteristic, Bipolar junction transistors and their working, Introduction to CB, CE & CC transistor configurations

Recommended Books:

- 1. Basic Electrical and Electronics Engineering, D.P. Kothari & I.J. Nagrath-Tata McGraw Hill
- 2. Basic Electrical and Electronics Engineering, S. K Bhattacharya -Pearson
- 3. Electrical Machinery- A.E. Fitzgerald, C. Kingsley and Umans TMH
- 4. Principles of Electrical Engineering-Vincdent Del Toro-Prentice Hall.
- 5. Basic Electrical Engineering -A,E. Fitzgerald, Higginbotham and Grabel -TMH
- 6. Integrated Electronics-Millmann & Halkias

Course Outcomes

After the completion of the course, the student will be able to –

- CO 1. Solve dc &ac circuits by applying fundamental laws & theorems
- CO 2. Compare the behavior of electrical and magnetic circuits for given input
- **CO 3.** Explain the working principle, construction, applications of rotating electrical machines
- **CO 4. Explain** the working principle, constructional details, losses & applications of single phase transformer.
- **CO 5.** Select the logic gates for various applications in digital electronic circuits.
- **CO 6. Explain** characteristics of Diode and Transistor.

(A Govt. Aided UGC Autonomous & NAAC Accredited Institute Affiliated to RGPV, Bhopal)

Basic Electrical & Electronics Engineering Lab (100022)

LIST OF EXPERIMENT

- 1. To verify Kirchhoff's Current Law & Kirchhoff's Voltage Law.
- 2. To verify Superposition Theorem
- 3. To determine resistance & inductance of a choke coil.
- 4. To determine active & reactive power in a single phase A.C circuit.
- 5. To determine voltage ratio & current ratio of a single phase transformer.
- **6.** To determine the polarity of a single phase transformer.
- 7. To perform open circuit & short circuit test on a single phase transformer.
- 8. To study multimeter& measure various electrical quantities
- 9. To study of constructional details of DC machine.
- 10. To determine the V-I characteristics of diode in forward bias & reverse bias condition.

Course Outcomes:

After the completion of the lab, the student will be able to -

- **CO 1.** Verify circuit theorems.
- **CO 2. Perform** tests on transformer for determination of losses, efficiency & polarity.
- **CO 3.** Acquire teamwork skills for working effectively in groups
- **CO 4. Prepare** an organized technical report on experiments conducted in the laboratory.

(A Govt. Aided UGC Autonomous & NAAC Accredited Institute Affiliated to RGPV, Bhopal)

B.Tech. I Semester (Electronics Engineering)

Subject Code	Category Code	Subject Name	Theory Slot			Prac	Total Mark	ı	Contact Hr/week		Total Credit	
			End Sem Marks	Mid Sem Marks	Quiz/ Assignment Marks	End Sem Mark	Lab work & Sessional Mark	s	L	T	P	S
2140122	DC	Electronics Devices	60	20	20	-	-	100	2	1	-	3

Electronics Devices (2140122)

Course Objective: To understand construction, principal and operation of different semiconductor devices.

Unit I: Fundamental of Electronic Devices: Elemental & Compound Semiconductor Materials, Bonding Forces and Energy Bands in Intrinsic and Extrinsic Silicon, Charge Carrier in Semiconductors, Carrier Concentration, Extrinsic Semiconductor, Hall Effect, Mechanism of Current Flow, Drift Current, Diffusion Current, Einstein Relation, Continuity Equation.

Unit II: Semiconductors Diodes: P-N Junction properties, Diode Characteristics, Equilibrium condition, biased junction, Steady state condition, P-N Junction breakdown mechanism, Capacitance of junction barrier, Diode circuit parameters, Basic circuits of Rectifier, Clippers and Clampers.

Unit III: Bipolar Junction Transistors: Construction, basic operation, current components and equations, CB, CE and CC configuration, input and output characteristics, Early effect, Region of operations: active, cut-off and saturation region.

Unit IV: Field effect transistors: Construction and characteristics of JFET, working principle of JFET.MOSFET construction and characteristics, MOSFET enhancement and depletion mode.

Unit V: Power Electronics Devices: Basic principle and working of SCR, IGBT, Uni-junction Transistor (UJT) and Thyristors. UJT: Principle of operation, characteristics.

Text Books:

- 1. Electronics Devices and Circuits: Boylested & Nashelsky, 11th Edition, Pearson Education India
- 2. Electronic devices and circuits: S. Salivahanan, 2nd Edition, Tata McGraw-Hill Education, 2011.
- 3. Microelectronic Circuits: Theory and Application: Sedra & Smith, 7th Edition, Oxford University Press.

Reference Books:

- 1. Micro Electronics: Millman, & Grabel, 2nd Edition, McGraw Hill Education
- 2. Integrated Electronics: Millman & Halkias, McGraw Hill Education.

Course Outcomes

After the completion of this course students will be able to:

- **CO1. Analyze** the properties of semiconductor materials.
- CO2. Understand construction and working of different diodes.
- CO3. Analyze the operation of Bi-polar junction transistors.
- **CO4. Examine** the working of Field Effect Transistors.
- **CO5.** Analyze the working of power electronics devices.

(A Govt. Aided UGC Autonomous & NAAC Accredited Institute Affiliated to RGPV, Bhopal)

B.Tech. I Semester (Electronics Engineering)

Subject Code	Category Code	Subject Name	Theory Slot			1				onta /we	Total Credit	
			End Sem Marks	Mid Sem Marks	Quiz/ Assignment Marks	End Sem Mark	Lab work & Sessional Mark	s	L	T	P	s
2140123	DC	Network Theory	60	20	20	-	-	100	2	1	-	3

Network Theory (2140123)

Course objective: To understand basic electric circuits, study of network theorems, transient analysis, graph theory, analysis of two port networks.

Unit-I Introduction — Basics of Circuit Elements, Characteristics of Independent & Dependent Sources, KCL & KVL for circuits with dependent & independent sources, Dot convention for coupled inductor and their characteristics, co-efficient of coupling.

Unit-II Network theorems: Superposition, Thevenin, Norton, Millman, Reciprocity and Maximum Power Transfer theorems.

Unit-III Laplace Transform & Passive Filters: The Laplace transform, Properties of Laplace transform, Initial and final value theorem. Waveform synthesis & Laplace Transform of various waveform function, Low pass, high pass, band pass and band elimination filters,

Unit-IV Transient analysis: Transients in RL, RC and RLC circuits, initial conditions, time constants, Steady state analysis, Node and mesh analysis of RL, RC and RLC networks with sinusoidal sources.

Unit-V Two Port Network: Concept of Ports, Calculation of network functions for one port and two port, Two port parameters -Z, Y, hybrid and chain Parameters, Relationship between two port network parameters, T and π networks, Characteristics impedance & propagation constant.

Text Books:

- 1. Network Analysis: M.E. Van Valkenberg, 3rd Edition, Prentice Hall of India.
- 2. Network and Systems: D. Roy Chaudhary, 2nd Edition, New Academic Science Ltd.

Reference Books:

- 1. Introduction to Modern Network Synthesis: M.E. Van Valkenberg, Prentice Hall of India.
- 2. Network Analysis & Synthesis: F. Kuo, 2nd Edition, Wiley & Sons.
- 3. Network Analysis & Synthesis: Ravish R Singh, 1st Edition, McGraw Hill Education.

Course Outcomes

After the completion of this course students will be able to:

- CO1. **Analyze** the circuits using Kirchoff's laws.
- CO2. Apply Network theorems for the simplification of circuits. .
- CO3. Apply the Laplace transform to linear circuits and systems.
- CO4. **Evaluate** transient response and steady state response.
- CO5. **Determine** ABCD, Z,Y and h parameters of an electrical circuit.

(A Govt. Aided UGC Autonomous & NAAC Accredited Institute Affiliated to RGPV, Bhopal)

B.Tech. I Semester (Electronics Engineering)

Subject	Categor	Subject Name	Theory Slot			Practical Slot			Total	Contact		ct	Total
Code	y Code	_					Mark	Hı	r/we	ek	Credit		
			End	Mid	Quiz/	End	Lab work	Skill	s	L	T	P	s
			Sem	Sem	Assignme	Sem	&	based					
			Marks	Marks	nt Marks	Mark	Sessional	mini					
							Mark	project					
2160122	ESC	Computer Programming	60	20	20	60	20	20	200	2	1	2	4

Computer Programming (2160122)

COURSE OBJECTIVES:

- To develop the understanding of algorithms, programming approaches and program documentation techniques.
- To study the concepts of procedural and object oriented programming.
- To design and implement basic programming solutions using programming constructs.

Unit I

Introduction to Programming, types of computer programming languages, Program Execution and Translation Process, Problem solving using Algorithms and Flowcharts. Introduction to C++ Programming: Data Types, Constants, Keywords, variables, input/output, Operators & Expressions, Precedence of operators.

Unit II

Control Statements and Decision Making: goto statement, if statement, if-else statement, nesting of if statements, The switch statement, while loop, do...while loop, for loop, nesting of for loops, break and continue statement. Function Basics, Function Prototypes, Passing Parameter by value and by reference, Default Arguments, Recursion. Arrays: One dimensional Arrays, Multidimensional Arrays, Passing Arrays to Functions.

Unit III

Strings, Pointers, Structures and File handling:, operations on Strings, Basics of Pointers & Addresses, reference variable, Pointer to Pointer, Pointer to Array, Array of Pointers, Pointer to Strings. Dynamic memory allocation using new and delete operators. Structures & Union, Pointer to Structure, Self-Referential Structures. File Concepts, Study of Various Files and Streams, operations on files.

Unit IV

Object Oriented Paradigm, Features of OOPS, Comparison of Procedural Oriented Programming with Object Oriented Programming, Abstract Data Types, Specification of Class, Visibility Modes, Defining Member Functions, Scope Resolution Operator, Constructors, its types, and Destructors, Creating of Objects, Static Data Member, Static Member Function, Array of Objects, Object as Arguments, Inline Function, Friend Function.

Unit V

Polymorphism: Introduction, Type of Polymorphism: Compile Time Polymorphism & Run Time Polymorphism, Function Overloading, Operator Overloading. Inheritance: Introduction, Visibility Modes, Types of Inheritance: Single Level, Multilevel, Multiple, Hybrid, Multipath.

RECOMMENDED BOOKS:

- C++ How to Program, H M Deitel and P J Deitel, Prentice Hall.
- Programming with C++, D Ravichandran, T.M.H.
- Computing Concepts with C++ Essentials, Horstmann, John Wiley.
- The Complete Reference in C++, Herbert Schildt, TMH.
- Object-Oriented Programming in C++, E Balagurusamy.
- Fundamentals of Programming C++, Richard L. Halterman.

COURSE OUTCOMES:

After completing this, the students will be able to:

CO1: identify situations where computational methods and computers would be useful.

(A Govt. Aided UGC Autonomous & NAAC Accredited Institute Affiliated to RGPV, Bhopal)

CO2: develop algorithms and flowchart for a given problem.

CO3: understand the concepts of procedural programming.

CO4: explain the concepts of object oriented programming and its significance in the real world.

CO5: analyze the problems and choose suitable programming techniques to develop solutions.

CO6: develop computer programs to solve real world problems.

(A Govt. Aided UGC Autonomous & NAAC Accredited Institute Affiliated to RGPV, Bhopal)

B.Tech. I Semester (Electronics Engineering)

Subject Code	Category Code	Subject Name	Theory Slot			1		Total Mark				Total Credit
			End Mid Sem Sem Marks Marks		Quiz/ Assignment Marks	End Sem Mark	Lab work & Sessional Mark	s	L	T	P	s
2140124	DLC	Devices & Network Lab	-	-	-	60	40	100	-	-	4	2

Devices & Network Lab (2140124)

Course Objectives:

Students will be able to learn the practical aspects of the basic electronic devices and also to verify the network theorems.

List of Experiment

- 1. Verify and plot the VI characteristic of PN junction and Zener Diode.
- 2. Design a half and full wave rectifier circuits.
- 3. Verify and plot the Input and Output characteristics of CE, CB,CC Configuration of BJT.
- 4. Verify and plot the Transfer and Output characteristics of CS, CG, CD Configuration of MOSFET.
- 5. Verification of KVL and KCL on bread board.
- 6. Verification of Thevenin, s & Norton, s Theorems.
- 7. Verification of Superposition Theorem.
- 8. Verification of Millman,,s Theorem.
- 9. Verification of Reciprocity Theorem.
- 10. Verification of Maximum Power Transfer Theorem.

Course Outcome:

After completing the course, students will be able to

- CO1. Verify the characteristics of diodes, BJT and MOSFET.
- CO2. Analyze circuits using Kirchauff's laws and Network theorems.

(A Govt. Aided UGC Autonomous & NAAC Accredited Institute Affiliated to RGPV, Bhopal)

B.Tech I Sem (Electronics Engineering)

Subject Code	Catego ry	Subject Name		The	ory Slot		Practical Slot			Total Marks	Contact Hr/week			Total Credits
	Code		End Sem Marks	Proficien cy	Mid Sem Marks	Quiz/ Assignment Marks	End Sem Mark	Lab work & Sessional Mark	Skill based mini project		L	T	P	
2140121	DC	Electronics Engineering Materials	50	10	20	20				100	2	1		3

Electronics Engineering Materials (2140121)

Course objective: To introduce the student with different material and their characteristics used in manufacturing of various electrical and electronics components

Unit I Introduction to Engineering Materials: Classification of Engineering Materials, Crystal Structure of Material, Level of Materials, Structure-Property Relationships in Material.

Unit II Conducting, Dielectric & Insulating Materials: Conducting Material- Properties of Conductor, Characteristics of Good Conductor Material, Definition and Classification of Dielectric and insulating materials, Superconductor.

Unit III Semiconductors: Introduction to Semiconductors and their properties, Effect of Temperature on Semiconductor, Mechanism of Conduction in Electrons and Holes, Carrier Generation and Recombination, Intrinsic Semiconductor & its Atomic Models, Extrinsic Semiconductor Material & its Atomic Models, Types of Impurity: Pentavalent and Trivalent Impurities, Majority and Minority Charge Carriers, Mobile Charge Carriers & Immobile Ions, Mass-Action Law.

Unit IV: Energy Levels & Bands: Atomic Structure, Bohr's Theory of Hydrogen Atom Excitation and Ionization of Atoms, Valence Band ,Conduction Band and Forbidden Energy Gap, Energy Band for Insulators, Semiconductors and Conductors, Fermi-Dirac Distribution Function, Fermi-Level in Intrinsic and Extrinsic Semiconductors, Energy Band gap.

Unit V Nanomaterial: Introduction of Nanomaterial, Classification of Nanomaterial, Electrical, Optical, Mechanical and Magnetic Properties, Methods for Creating Nanostructures, Application & Advantages.

Text Books:

- 1. S.K Bhattacharya,"Electrical and Electronic Engineering Material", Ist edition, Khanna Publishers, Delhi, 2006.
 - 2. A. J. Dekker, "Electrical Engineering Material", Reprint Ist Edition, PHI, 2006.
 - 3. Nanomaterial-B. Vishwanatham, published by Narosa Publishing House.

Reference Books:

- 1. Sahdey," Electrical Engineering Material", Unique International Publications.
- 2. C.S Indulkar & S. Thiruvengadam, "Electrical Engineering Material", ReprintIst Edition, 2013, S. Chand & Com.Ltd.New Delhi-55.
- 3. S.P Seth, P. V. Gupta, "A course in Electrical Engineering Materials", 4th Edition,2017, Dhanpat Rai & Sons.

Course Outcome:

After successful completion of the course, student will be able to:

- **CO1.** Classify Engineering Materials.
- CO2. Analyze the characteristics of conducting, dielectric and insulating materials.
 CO3. Analyze the characteristics of Semiconducting Materials.
 CO4. Describe the Energy Level for Semiconductor Materials.
 CO5. Describe Nanomaterials with their ap